

2814

AMENDMENT TRANSMITTAL LETTER (Large Entity)

Applicant(s): Tsukasa Yajima

Docket No.

PNET.009D

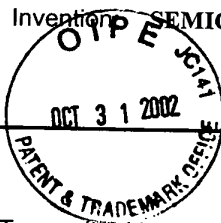
Serial No.
09/768,271

Filing Date
January 25, 2001

Examiner
A. Mai

Group Art Unit
2814

Invention: SEMICONDUCTOR DEVICE HAVING PROTECTIVE LAYER ON FIELD OXIDE



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TO THE ASSISTANT COMMISSIONER FOR PATENTS:

Transmitted herewith is an amendment in the above-identified application.

The fee has been calculated and is transmitted as shown below.

CLAIMS AS AMENDED

	CLAIMS REMAINING AFTER AMENDMENT	HIGHEST # PREV. PAID FOR	NUMBER EXTRA CLAIMS PRESENT	RATE	ADDITIONAL FEE
TOTAL CLAIMS	13 -	20 =	0 x	\$18.00	\$0.00
INDEP. CLAIMS	3 -	3 =	0 x	\$84.00	\$0.00
Multiple Dependent Claims (check if applicable) <input type="checkbox"/>					\$0.00
TOTAL ADDITIONAL FEE FOR THIS AMENDMENT					\$0.00

- ☒ No additional fee is required for amendment.
- ☐ Please charge Deposit Account No. _____ in the amount of _____
A duplicate copy of this sheet is enclosed.
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 - ☒ Any additional filing fees required under 37 C.F.R. 1.16.
 - ☒ Any patent application processing fees under 37 CFR 1.17.

Signature

Dated: October 31, 2002

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I certify that this document and fee is being deposited on _____ with the U.S. Postal Service as first class mail under 37 C.F.R. 1.8 and is addressed to the Assistant Commissioner for Patents, Washington, D.C. 20231.

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In re PATENT APPLICATION of

Tsukasa Yajima

Group Art Unit: 2814

Serial No.: 09/768,271

Examiner: A. Mai

Filed: January 25, 2001

For: SEMICONDUCTOR DEVICE HAVING PROTECTIVE LAYER ON FIELD OXIDE

AMENDMENT

Honorable Assistant Commissioner for Patents
Washington, D.C. 20231

Date: October 31, 2002

Sir:

In response to the Office Action dated July 31, 2002, the following amendments and remarks are respectfully submitted in connection with the above-identified application.

In the Specification:

Replace the paragraph beginning on page 13, line 12 with the following paragraph:

“Thereafter, the side-wall 37 is formed by implementing the anisotropic etching such as the RIE with respect to the oxide layer 36 (FIG. 1(h)). Then, the insulating layer 50 and the contact holes 52 are formed, and the aluminum wiring 54 is conducted by the normal processes, thus completing the MOSFET (FIG. 1(i)).”

In the Claims: